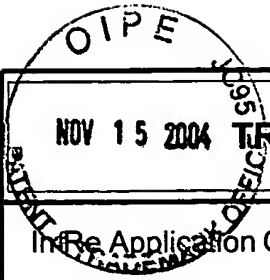
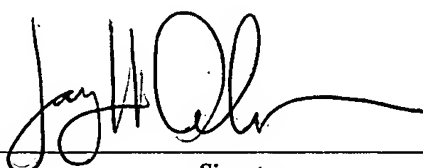
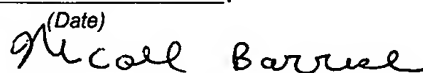


1FW

				<b>TRANSMITTAL OF FORMAL DRAWINGS</b>		<b>Docket No.</b> <b>FIS920030341US1</b>	
In Re Application Of: <b>Victor Ku et al.</b>							
Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.		
10/707,759	01/09/2004		32074	2812	1758		
<b>Invention: METHOD OF FORMING FET SILICIDE GATE STRUCTURES INCORPORATING INNER SPACERS</b>							
<p style="text-align: center;">Address to:  <b>Commissioner for Patents</b>  <b>P.O. Box 1450</b>  <b>Alexandria, VA 22313-1450</b></p>							
Transmitted herewith are:  8 sheets of formal drawing(s) for this application.							
<input checked="" type="checkbox"/> Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).							
 _____ Signature				Dated: 12 November 2004			
<b>Jay H. Anderson, Reg. No. 38,371</b> <b>IBM Corporation, IP Law</b> <b>2070 Route 52, Bldg. 300, Zip 482</b> <b>Hopewell Junction, New York 12533-6531</b> <b>845-894-3667</b>				<div style="border: 1px solid black; padding: 5px;">         I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on          11/12/2004          _____          (Date)            Signature of Person Mailing Correspondence  <b>Nicole Barrese</b>          Typed or Printed Name of Person Mailing Correspondence       </div>			